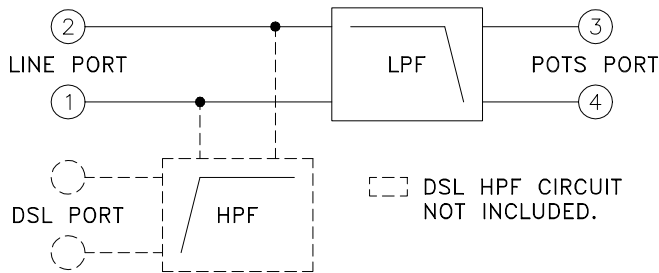
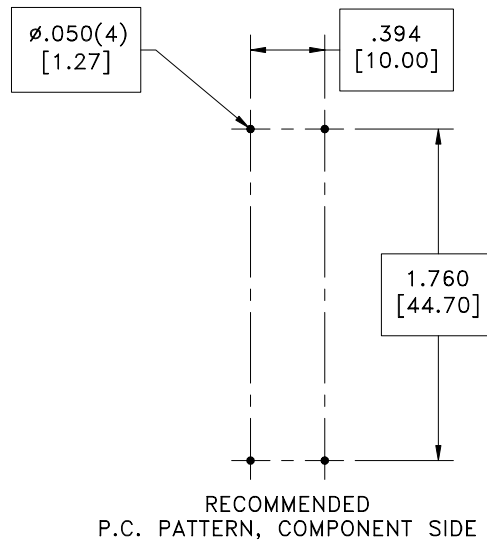


SOLDER SUFFIX	CUSTOMER TERMINAL	RoHS	LEAD(Pb)-FREE
	Sn63%, Pb37%	No	No
LF1	Sn96%, Ag4%	Yes	Yes



**ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:**

- D.C. RESISTANCE (@20°C): 40 ohms max., 1-2(tie 3+4).
  - ⑥D INSERTION LOSS: -1.0dB max., 1000Hz @ 80mA loop current. Within 1.0dB, 200 - 1000Hz (relative to 1kHz); 1000 - 3400Hz (relative to 1kHz).
  - RETURN LOSS: -12dB min., 200 - 3400Hz. -8dB min., 3400 - 4000Hz.
  - POTS GROUP DELAY: 200us max., 200 - 4000Hz.
  - LONGITUDINAL BALANCE: 40dB min., 50 - 600Hz; 3400Hz - 30kHz. 46dB min., 600 - 3400Hz. 45dB min., 30 - 2208kHz.
  - STOP BAND ATTENUATION: -55dB min., 32k - 2208kHz.
  - LINE IMPEDANCE: 600 ohm, 270 ohm + 750ohm//150nF.
  - LOOP CURRENT: 80mADC.
  - OPERATING TEMPERATURE RANGE: 0°C to +70°C.
- Designed to comply with ETSI TS 101 952-1-1, Option A ADSL/POTS Splitter.  
Midcom p/n 681-2026-LF1 is eiSos p/n: 750092026.

Unless otherwise specified:  
Tolerances:  
Angles: ±1°  
Fractions: ±1/64  
Decimals: ±.005[.13]

This drawing is dual dimensioned.  
Dimensions in brackets are in millimeters.

DRAWING TITLE

**DSL/POTS SPLITTER**

eiSos p/n: 750092026

REVISIONS: SEE SHEET 1

Midcom www.midcom-inc.com

DRAWING NO.

**681-2026/-LF1**

SCALE ---

REV.

6E  
7/07

SHEET 2 OF 12

DWG # 681-2026/-LF1